



Classification and Comparison of Scalar Multiplication

Algorithms in Elliptic Curve Cryptosystems

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Abstract: The most popular public-key cryptography systems nowadays are RSA and Elliptic Curve Cryptography (ECC). ECC is a type of public-key cryptosystem which uses the additive group of points on a nonsingular elliptic curve as a cryptographic medium. The basic operation in most elliptic curve cryptosystems is a scalar multiplication. Scalar Multiplication is the costliest operation among all in ECC which takes 80% of key calculation time on Elliptic curve calculation. Hence Scalar multiplication is the most time-consuming operation in ECC protocols. Scalar multiplication (or point multiplication) is the operation of calculating an integer multiple of an element in additive group of elliptic curve. in this paper, we classify and compare proposed scalar multiplication algorithms and compute their executing time.

Keywords: ECC, Cryptography, Elliptic Curve, Scalar Multiplication

1. Introduction

Compared with other approaches, such as the widely used Rivest-Shamir-Adleman (RSA) public key system, the Elliptic Curve Cryptography public key system is more appropriate for embedded systems with limited power and memory resources.

ECC is considered much more suitable than other public-key algorithms. It consumes lower power, has better performance and can be implemented in small areas that can be achieved by ECC. In this paper, the Elliptic Curve properties are carefully analyzed.

In fact, multi-scalar multiplications are the most time consuming operations for elliptic curve cryptosystems where implementations are mainly on devices with constrained computational power and memory (i.e. smart card), therefore, efficient operations are essential. It is denoted by kP where P is a curve point and k a scalar one. Basic scalar multiplication algorithm scans each bit of k and performs some curve-level operations based on

the bit value. Scalar representation significantly impacts the number of point operations to be executed and overall computation time representation of elliptic curve elements.

Multi-scalar multiplication is required in many elliptic curve cryptosystems (ECC) such as the verification process of ElGamal digital signature, verification process of ECDSA, provable-secure digital signatures [3,4], multi-party protocols [1] and protocols of Brands [2].

Hence, efficiency of multi-scalar multiplications is essential in elliptic curve cryptosystems. The major building block of most elliptic curve cryptosystems is the computation of multi-scalar multiplication.

Scalar multiplication corresponds to group element exponentiation in a multiplicative group, i.e. x^k , for some x in the multiplicative group. Therefore, one can easily adapt classical exponentiation methods to scalar multiplication, replacing multiplication by addition as well as squaring by doubling. Mathematicians have dealt

with exponentiation methods for more than two thousand years and efficient methods have since developed.

In this paper, we will present numerous scalar multiplication methods starting from generic ones up to curve specific ones. There are two main conditions that occur in practice: where P is a prime number and where it is not there is also a sub case in which the scalar is used several times. Additionally, different coding methods of k result in different scalar multiplication methods. The expected running time and the worst case running time are important to analyze a scalar multiplication method. Thus, one of them is always given in details when algorithm is coded. Algorithms are constructed for the P points in $E(F_q)$ for some $q = p^m$ where p is prime and m is in \mathbb{Z}^+ , since cryptosystems are designed on $E(F_q)$.

The following sections of this paper are organized as follows. Section 2 contains the introduction to a number of generic methods.

Different methods based on the concept of fixed point would be explained in section 3. Section 4 studies scalar multiplication methods on some specific curves. The notion of endomorphism would be described in section 5. Section 6 is consisted of the conclusion and results.

2. Unknown Point

In this section, both k and P are unknown until the run-time, i.e. they are placed into the program at the run time. Since k and P may vary, methods given in this section may be realized as generic methods.

2.1. Binary Method

Binary method is the first known exponentiation method; therefore, it is a scalar multiplication method, too. Binary representation of the scalar multiplication enables us to interpret the multiplication as the cumulative addition of non-zero components. Namely, if k has the binary representation $(k_{i-1}, k_{i-2}, \dots, k_0)_2$ where $k_i \in \{0,1\}$, then $k = \sum_{i=0}^{l-1} k_i 2^i$.

$$kP = \sum_{i=0}^{l-1} k_i 2^i P \quad (2.1)$$

$$= k_0P + k_12^1P + \dots + k_{l-1}2^{l-1} \quad (2.2)$$

$$= k_0P + 2(k_1P + 2(k_2P + \dots + (2(k_{l-2}P + 2(k_{l-1}P))$$

Equation 2.2 can be interpreted as starting from k_0 and summing up the terms k_i2^iP cumulatively for each non-zero k_i up to k_{l-1} to end up with kP . 2^iP can be calculated by $2 * 2^{i-1}P$ if $2^{i-1}P$ is known. Thus, in order to speed up, 2^iP must be calculated for each $i \in \{0, \dots, l-1\}$ by doubling the previous one, and adding it to the cumulative sum and if k_i is 1. Because of which, this case of the method is known as double-and-add. Also, it is called right-to left binary method since it starts from k_0 and ends with k_{l-1} . The pseudo-code of right-to-left binary method is Algorithm 2.1.

Equation 2.3 enables us to interpret the multiplication as starting from k_{l-1} down to k_0 and adding P if k_i is non-zero and continuously doubling whatever k_i is. Contrary to the previous case, it is not necessary to keep at hand a doubled version of P . In other words, memory is not allocated for a doubled version of P . Because of similar reasons, this case of the method is

known as add-and-double or left-to-right binary method. Its pseudo-code is given in the Algorithm 2.2.

Algorithm 2.1.[6]

Right-to-left binary method for scalar multiplication

Input: $k = (k_{l-1}, k_{l-2}, \dots, k_0)_{2, P \in E(Fq)}$.

Output: kP

1. $Q = \infty$.
2. for i from 0 to $l-1$ do
 - 2.1. if $k_i = 1$ then $Q = Q + P$.
 - 2.2. $P = 2P$.
3. output(Q)

Algorithm 2.2.[7]

Left-to-right binary method for scalar multiplication

Input: $k = (k_{l-1}, k_{l-2}, \dots, k_0)_{2, P \in E(Fq)}$.

Output: kP

1. $Q = \infty$.
2. for i from $l-1$ down to 0 do
 - 2.1 $Q = 2Q$
 - 2.1 if $k_i = 1$ then $Q = Q + P$.

3. Output (Q)

The running time of an algorithm is determined by the number of operations being performed throughout its execution. In order to do that, it is essential to analyze each line of the algorithm in detail. After the analysis, the expected running time of Algorithm 2.1 is $1/2 \text{Additions} + 1 \text{ Doubling}$ denoted as

$$\frac{l}{2A} + lD \quad (2.4)$$

Algorithm 2.2 has the same operations as Algorithm 2.1 with the reverse order, so they have the same running time.

2.2. Non-Adjacent Form (NAF)

Using the information from previous section, we know that the inverse of $P = (x, y) \in E(Fq)$ is $-P = (x, x + y)$ in binary fields and $-P = (x, -y)$ in the fields of characteristic $\gg 3$. Thus, taking the inverse of an element on elliptic curve is very fast in terms of computational time. This brings up the question of representing k in the form

$$k = \sum_{i=0}^{l-1} k_i 2^i \text{ where } k_i \in \{-1, 0, 1\} \quad (2.5)$$

Algorithm 2.3 to get fast computation for kP .

When minus one comes across, subtraction of P is performed in addition to binary method during the scalar multiplication kP . A representation whose set also consists of negative values is called Signed Digit Representation (SDR). If the representation set is $\{-1, 0, 1\}$, then it is the most trivial type of signed digit representation known as signed binary representation. In the binary method, we have noticed that the running time of algorithm increases proportional to the number of 1s in its representation. Hence, the aim is to form a representation of an integer k whose weight (number of nonzero elements) and length is as small as possible. There is a representation which satisfies this aim:

Definition 3.4.A. the non-adjacent form (NAF) of a positive integer k is an expression $k = \sum_{i=0}^{l-1} k_i 2^i$ where $k_i \in \{-1, 0, 1\}$, $k_{l-1} \neq 0$ and no two consecutive digits are nonzero.

According to this definition, the length of NAF

is 1. For any $k \geq 1$, NAF exists and has the following properties:

Theorem 3.5. Let k be a positive integer, then

1. k has a unique NAF denoted as $NAF(k)$ [8].
2. $NAF(k)$ has the fewest nonzero digits among the signed binary representations of k [8].
3. The expected value of the 1s of $NAF(k)$ over the length of $NAF(k)$ is $1/3$ [9].
- iv. The length of $NAF(k)$ is at most "one" more than the length of the binary representation of k [9].

Algorithm 3.6 gives the recoding of a given integer k into $NAF(k)$. Then, Algorithm 2.5, which takes $NAF(k)$, computes the scalar multiplication kP using NAF representation of k and is called NAF method. We will, first, give the algorithms and then explain the steps involved.

Algorithm 2.3.[8]

Right-to-left NAF recoding

Input: $k = (k_{l-1}, k_{l-2}, \dots, k_0)_2$

Output: $NAF(k)$

1. $C_0 = 0$

2. For $i = 0$ to l do

2.1. $c_{i+1} = \lfloor (c_i + k_i + k_{i+1})/2 \rfloor$

2.2. $k'_i = c_i + k_i - 2c_{i+1}$

3. *output* (k'_1, \dots, k'_0)

However, as we have seen in binary method, left-to-right scalar multiplication is more efficient than right to left one in terms of memory consumption.

Algorithm 2.4.[10]

Left-to-Right NAF recoding

Input: $(k_{l-1}, \dots, k_0)_2$

Output: $NAF(k)$

1. $j = m, b = 0, k_l = 0$

2. For i from $l-1$ down to 0 do

2.1. If $(k_{i+1} = k_i)$ then

2.1.1. $k'_j = k_i - b$

2.1.2. while $(j > i + 1)$ do

2.1.2.1.

$$j = j - 1, k'_j = 1 - k_i - b, b = 1 - b$$

2.1.3. $b = k_i, j = j - 1$

3. $k'_j = -b$

4. while (j >0) do

4.1. $j = j - 1, k'_j = 1 - b, b = 1 - b$

5. output (k'_1, \dots, k'_0)

Algorithm 2.5. Left-to-right NAF multiplication

input: $NAF(k) = (k_{l-1}, k_{l-2}, \dots, k_0)$ and $P \in E(F_q)$

output: $kP \in E(F_q)$

1. $Q = \infty$

2. For i from $l - 1$ down to 0 do

2.1. $Q = 2Q$

2.2. If $k_i = 1$ then $Q = Q + P$

2.3. If $k_i = -1$ then $Q = Q - P$

3. Output (Q)

Note that line 2.1 of Algorithm 2.5 performs exactly l time and by Theorem 3.5 (iii) and (iv).

It is expected that lines 2.2 and 2.3 together perform approximately $1/3$ times. Therefore, expected running time of Algorithm 2.5 is:

$$l/3A + lD \tag{2.6}$$

2.3. Window Method

If the digits of k representation are allowed to be the elements of a larger set instead of only $\{-1,$

$0, 1\}$, then, the running time of the above algorithms decrease. In this case, not only P is added or subtracted, but also any small scalar multiple of P is added or subtracted. So, those values have to be calculated at the beginning of the scalar multiplication algorithm and saved to the memory. The window method may be interpreted as processing some consecutive digits of the scalar at a time. There are unsigned and signed versions of the window method. Unsigned width- w window representation of a positive integer k is $k = \sum_{i=0}^{l-1} k_i 2^i$ where k_i is either zero or an odd integer smaller than 2^w and $k_{l-1} \neq 0$. Similarly, signed width- w window representation of a positive integer k is $k = \sum_{i=0}^{l-1} k_i 2^i$ where $|k_i|$ is either zero or an odd integer smaller than $2^{w-1}, k_{l-1} > 0$ and particularly width- w NAF representation of a positive integer k is $k = \sum_{i=0}^{l-1} k_i 2^i$ where $|k_i|$ is either zero or an odd integer smaller than $2^{w-1}, k_{l-1} > 0$. Since width- w NAF reduces nonzero terms fairly, we will only deal with properties and running time

of it. In order to ensure that w consecutive digits contain at most one nonzero digit, reduction module 2^w has to be conducted by choosing the least residue as represented in line 2.2 of Algorithm 2.6.

Algorithm 2.6.[11]

Computing the NAF_w of a positive integer

Input: window width w , positive integer k

Output: $NAF_w(k)$

1. $i = 0$
2. While $k > 0$ do
 - 2.1. If k is even $k_i = 0$
 - 2.2. Else $k_i = k \bmod 2^w, k = k - k_i$
 - 2.3. $K = k/2, i = i + 1$
3. Output $(k_{l-1}, \dots, k_1, k_0)$

As in the binary method and NAF method, if one can do the recoding of $NAF_w(k)$ left-to-right, scalar multiplication operation using $NAF_w(k)$ can be performed on-the-fly. That is, scalar multiplication and recoding operations can be performed simultaneously.

Avanzi[24], Muir et al.[12] and Okeya et al.[13] independently obtained similar results of left-to-

right recoding of an integer k having the least Hamming weight. [12]’s left-to-right algorithm is optimal, and different from Avanzi’s and it can output up to two different recordings of the same integer, one of which is equal to that of Avanzi’s algorithm, whereas the other one differs in some of the less significant digits. Okeya et al. [14] also have a left-to-right algorithm, which is not equivalent to the w -NAF, but only gives asymptotic density estimates using Markov chains.

Algorithm 2.7.[11]

Left-to-right $NAF_w(k)$ multiplication

Input: $NAF_w(k), P \in \mathbb{C}\mathbb{E}(\mathbb{F}_q)$

Output: kP

0. Compute $2P$
1. For $i = 3$ up to $2^{w-1} - 1$ do
 - 1.1 if i is odd then compute $P_i = P_{i-1} + 2P$
2. $Q = \infty$
3. for $i = l-1$ down to 0 do
 - 3.1. $Q = 2Q$
 - 3.2. if $k_i \neq 0$ then

3.2.1. if $k_i > 0$ then $Q = Q + P_{k_i}$

3.2.2. else $Q = Q - P_{-k_i}$

4. Output (Q)

Running time of line 0 and line 1 of Algorithm 2.7 is $1D$ and $(2^{w-2} - 1)A$ respectively; pre-computation cost, therefore, is $1D + (2^{w-2} - 1)A$. Next, the running time of line 3.1 is $1D$ and the expected total running time of line 3.2.1 and line 3.2.2 is $(l/(w + 1))A$. To sum up, the expected running time of Algorithm 2.7 is

$$(l + 1)D + (2^{w-2} + \frac{l}{w+1} - 1)A. \quad (2.7)$$

2.4. Sliding Window Method

This method operates left-to-right over the digits of k with a maximum window width of w , at which the value in the window is odd. In contrast to window method, it has no exact window width; but similar to window method, it ignores zero digits. This method can be applied to binary or NAF representation of k . It may be applied to $NAF_w(k)$ with the maximum window width of

w' , but, the same algorithm of window NAF is obtained unless $w' > w$.

Algorithm of sliding window method applied to NAF_2 is given in Algorithm 2.8. So, it has to be computed first, and then placed into the algorithm. The first stage is the pre-computation of P_i for some i . Observe that a block of digits in an arbitrary window may have a maximum value of either $101010 \dots 10101$ (w -digits) or $101010 \dots 101001$ (w -digits) if w is odd or even respectively. Therefore, the value of the highest value block is either $(2^{w+1} - 1)/3$ or $(2^{w+1} - 5)/3$ which implies that the upper bound for the pre computation stage is :

$$2 \frac{2^w - (-1)^w}{3} - 1 \quad (2.7.1)$$

Algorithm 2.8.[15]

NAF Sliding window method for scalar multiplication

Input: window width w , $NAF(k)$, $P \in \mathbb{C}(F_q)$

Output: kP

0. $P_1 = P$

1. compute $2P$

2. For i from 3 to $2(2^w - (-1)^w)/3 - 1$

2.1. $P_i = P_{i-2} + 2P$

3. $Q = \infty, i = \text{length}(\text{NAF}(k)) - 1$

4. *while* $i \geq 0$ *do*

4.1. *if* $ki = 0$ *then* $t = 1, u = 0$

4.2. *else*

4.2.1. $t = 1; j = w$

4.2.2. *while* ($t = 1$ and $j > 1$) *do*

4.2.2.1. *if* ($ki, \dots, ki-j+1$) *is odd* $t = j;$

$u = (ki, \dots, ki-j+1)$

4.3. $Q = 2^t Q$

4.4. *if* $u > 0$ *then* $Q = Q + Pu$; *else if* $u < 0$ *then*

$Q = Q - P-u$

4.5. $i = i - t$

5. Output (Q)

The average length of a run of zeros between windows in the NAF sliding window method is stated in [9] as

$$v(w) = \frac{4}{3} - \frac{(-1)^w}{3 \cdot 2^{w-2}} \quad (2.8)$$

Pre-computation stage consists of lines 0, 1 and

2. Line 1 costs $1D$ and line 2 costs $((2^w - (-1)^w)/3 - 1)A$. Thus, totally

$$\left(1D + \frac{2^w - (-1)^w}{3} - 1\right) \quad (2.8.1)$$

is done at the pre-computation stage. Next, lines 4.1, 4.2 are worthless. Line 4.3 is executed once, hence costs $1D$. In order to find the running time of line 4.4, we need to find the average number of nonzero terms and the number of zeros between the windows. The average length of a run of zeroes between windows in the NAF sliding window method is

$$v(w) = \frac{4}{3} - \frac{(-1)^w}{3 \cdot 2^{w-2}} \quad (2.8.2)$$

The expected cost of line 4.4 is

$$\frac{l}{w+v(m)} \quad (2.8.3)$$

Therefore, the expected running time of Algorithm 2.8 is:

$$(1 + l)D + \left(\frac{2^w - (-1)^w}{3} + \frac{l}{w+v(m)} - 1\right)A \quad (2.8.4)$$

2.5. Montgomery's Ladder

Montgomery [17] presented a ladder method to perform fast exponentiation (scalar multiplication). After that, [16] presented the elliptic curve version of Montgomery's ladder. However, their ideas were applicable only for

non-super singular curves over binary fields. [14] Extended Montgomery's ladder method for elliptic curves over non-binary fields. Hence, it became a generic method for computing scalar multiplication on elliptic curves.

The general idea of this method is to start from the left-most bit of the scalar and a pair $(P, 2P)$ corresponding to the left-most bit. Then, iterate to the next left-most bit with a pair $(2P, P + 2P)$ (i.e. doubling first component of the previous pair and adding the first and second component of previous pair) or $(P + 2P, 2(2P))$ (i.e. addition of first and second component of previous pair and doubling second component of previous pair) if the next left-most bit of the scalar is 0 or 1, respectively. Continue this procedure until reaching the last bit and naturally the pair $(kP, (k + 1)P)$. This idea is called scalar multiplication equivalence of exponentiation. In the above iteration, it is enough to compute x-coordinate of both components for each pair. Each iteration requires only an addition and a doubling. Also,

there is a shortcut for the addition operation for elliptic curves over binary fields: For any given $Q_i = (x_i, y_i)$ $i= 1, 2, 3, 4$ satisfying $Q_3 = Q_1 + Q_2$ and $Q_4 = Q_1 - Q_2$. Then,

$$x_3 = x_4 + \frac{x_2}{x_1 + x_2} + \left(\frac{x_2}{x_1 + x_2}\right)^2$$

In our case, $Q_1 = (1 + 1)P$, $Q_2 = 1P$ and $Q_4 = P$ are given and we are asked to compute $(2l+1)P = Q_3$. Therefore, this shortcut could be applied at each iteration stage. Moreover, the y-coordinate of kP can be recovered, if needed, as: $y_1 = x^{-1}(x_1 + x)[(x_1 + x)(x_2 + x) + x^2 + y] + y$, where $kP = (x_1, y_1)$, $(k + 1)P = (x_2, y_2)$ and $P = (x, y)$.

Algorithm 2.9.[16]

Montgomery scalar multiplication

Input: $k = (k_{l-1}, \dots, k_1, k_0)$, $P \in E(F_q)$

Output: kP

1. $X = P$ and $Y = 2P$
2. for i from $l - 2$ down to 0 do
 - 2.1. if $k_i = 0$ then $X = 2X$ and $Y = X + Y$
 - 2.2. else $X = X + Y$ and $Y = 2Y$
3. Output(X)

Algorithm 2.9 performs a doubling and an addition for each i whatever k_i is. Thus, the expected running-time of the algorithm is $l(D + A)$. However, it is not necessary to compute the y -coordinate of corresponding points until the last iteration. Hence, it is not possible to compare the running-time of this algorithm with the running-time of the others.

2.6. Non-Adjacent Form (2-NAF) Method

In [26], NAF is a signed representation that uses elements from the set $\{-1, 0, 1\}$. It is a canonical representation with the smallest number of non-zero digits for any scalar d , denoted by $NAF(d)$ and it contains at most *one* non-zero digit among any two successive digits. Moreover, the length of $NAF(d)$ is at most *one* more bit than its binary representation.

To compute scalar multiplication of a scalar d , NAF performs two algorithms. The first algorithm converts the scalar to a signed

representation while the second algorithm computes scalar multiplication.

The first algorithm (algorithm 2.10) computes the NAF of a scalar d if w is fixed to two and the second (algorithm 2.11) uses obtained NAF for scalar multiplication when $w = 2$.

Algorithm 2.10 Computing the 2-NAF of a positive integer

Input: window w ($w = 2$), scalar d

Output: $NAF_w(d)$

1. $i = 0$
2. *While* $d \geq 1$ *do*
 - 2.1. If d is odd, then $d_i = \text{fct}(d, 2w)$, $d = d - d_i$
 - 2.2. else $d_i = 0$
 - 2.3. $d = d/2$, $i = i + 1$
3. *Return* $(d_{i-1}, \dots, d_1, d_0)NAF_w$

Algorithm 2.11 2-NAF method for scalar multiplication

Input: Window w ($w = 2$), scalar $d = (d_{i-1}, \dots, d_1, d_0)$

PE (F_p)

Output: dP

1. Compute $P_i = iP$ for $i \in \{1, 3, 5, \dots, (2^l - 1)\}$

2. $Q = O$

3. For $i = l - 1$ down to 0 do

3.1. $Q = 2Q$

3.2. if $d_i > 0$ then $Q = Q + P d_i$

3.3. $Q = Q - P d_i$

4. Return (Q)

Function $fct(d, 2^w)$

If $d \bmod 2^w > 2^{w-1}$ then $d_i = (d \bmod 2^w) - 2^{w-1}$

Else $d_i = d \bmod 2^w$

Let l be the bit-length of a scalar d , the expected number of doublings and additions using algorithm 2.2 is approximately $(l - 1)$ and $l/3$, respectively. Thus the cost of the NAF method is:

$$(l-1) \cdot D + (l/3) \cdot A \tag{2.9}$$

3. Fixed Point

It was mentioned earlier in this paper that fixed point is the point for which a scalar multiplication is to be computed, and so the algorithm of scalar multiplication can be

designed according to this privilege. For instance, if point P is fixed and some storage is available, then some of the multiples of P can be pre-computed and saved to memory, and then used during the computation of scalar multiplication directly by memory call.

3.1. Fixed-Base Windowing Method

If P is fixed, then the simplest idea that can be applied to the scalar multiplication is pre-calculation of all doublings of P up to $2^t P$ i.e. $2P, 4P, 8P, \dots, 2^{t-1}P$ where t is equal to approximately extension degree m of our finite field. Then, for any given scalar k , one can compute kP by summing up only $2^i P$ for which k_i is nonzero. Hence, all doublings are removed, and expected running time of binary algorithms decreases to $(l/2) \cdot A$. A refinement to the above idea is first described in [18] and more refinement is given in section 4.6.3 of [6]. Finally, [19] proposed a patented version of previous ideas. The basic idea behind these refinements is the following equality:

$$KP = \sum_{i=0}^{l-1} k_i P = \sum_{j=1}^{l-1} \left(j \sum_{i:k_i=j} 2^{wi} P \right) \text{ where } k = \sum_{i=0}^{d-1} K_i 2^{wi} \quad (3.1)$$

Output: kP

1. $A = \infty, B = \infty$

2. For j from $2^w - 1$ down to 1 do

2.1. for i from 0 up to $d - 1$ do

2.1.1. if $j = K_i$ do $B = B + P_i$

2.2. $A = A + B$

3. Output (A)

We now define the BGMW's algorithm. Let

$$k = (k_{l-1}, \dots, k_1, k_0)_2 = (k_{d-1} \parallel \dots \parallel k_0) \\ = (k_{d-1}, k_{d-2}, \dots, k_0)_{2^w} \sum_{i=0}^{d-1} k_i 2^{wi} \quad (3.2)$$

Then

$$KP = \sum_{i=0}^{d-1} k_i 2^{wi} P \\ = \sum_{i=0}^{d-1} k_i (2^{wi} P) \\ = \sum_{j=0}^{2^w-1} \left(j \sum_{i:k_i=j} 2^{wi} P \right) \\ = \sum_{j=1}^{2^w-1} j Q_j \quad (3.3)$$

$$= Q_{2^w-1} + (Q_{2^w-1} + Q_{2^w-2}) + \dots + (Q_{2^w-1} + Q_{2^w-2} + \dots + Q_1). \quad (3.4)$$

Then, the corresponding algorithm is coded as, first, calculate Q_j by cumulative addition of K_i s and add Q_j s together cumulatively to reach kP.

Algorithm 3.1.[19]

Binary BGMW's algorithm

input: $w, d = \lceil l/w \rceil, k = (K_{d-1}, K_{d-2}, \dots, K_0)_{2^w}, P \in E(Fq)$

precomputed values: $P_i = 2^{wi} P_i, 0 \leq i \leq d - 1$

The pre-computation stage is not included in the running-time. The Expected running-time of line 2.2.1 of Algorithm 3.1 is $(d/(2^w - 1))(2^w - 1)$ since the equation $j = K_i$ occurs expectedly $d/(2^w - 1)$ times and the outer loop performs this expectancy $2^w - 1$ times. Consequently, the addition at line 2.2.1 performs d times. In fact, it costs $(d - 1)A$ if we discard the trivial first addition which is the identity plus a point. line 2.2 performs $2^w - 1$ times, but costs $(2^w - 2)A$ by discarding the first trivial addition. Therefore, the expected running time of Algorithm 3.1 is $(2^w + d - 3)A$, where $d = \lceil l/w \rceil$

Algorithm 3.2 uses the same argument presented above with NAF representation of k instead of binary representation. $NAF(k) = (K_{d-1}, \dots$

, K_1, K_0) where each K_i is in non-adjacent form.

Thus, K_i would be maximum if w is even or odd, respectively.

$$\frac{(1010 \dots 1010)_{NAF2}}{2^{w+1}-2} \text{ or } \frac{(1010 \dots 101)_{NAF2}}{3} = \frac{2^{w+1}-1}{3} \tag{3.5}$$

Algorithm 3.2.[20]

NAF BGMW's algorithm

input: $w, NAF(k), P \in E(Fq)$

pre-computed values: $P_i = 2^{(wi)}P, 0 \leq i \leq \lceil (l+1)/w \rceil$

output: kP

1. $d = \lceil l/w \rceil$
2. if w is even $S = (2^{w+1} - 2)/3$ and else $S = (2^{w+1} - 1)/3$
3. $A = \infty, B = \infty$
4. for j from S to down to 1 do
 - 4.1. for i from 0 up to $d-1$ do
 - 4.1.1. if $j = K_i$ do $B = B + P_i$
 - 4.1.2. else if $-j = K_i$ do $B = B - P_i$
 - 4.1.3. $A = A + B$
5. output(A)

, the expected running time of Algorithm 3.2 is

$$(2^{w+1}/3 + d - 2)A \text{ where } d = \lceil l/w \rceil. \tag{3.6}$$

3.2. Fixed-Base Comb Method

This method is also known as Lim-Lee Method.

Let $d = \lceil l/w \rceil$. If necessary, pad on the left side of the representation with zeros and then, split k into w concatenated parts: $k = K^{w-1}, \dots, K^0$.

$$\begin{aligned} KP &= \sum_{i=0}^{l-1} k_i 2^i P \\ &= k_0P + k_12P + k_24P + \dots + k_{l-1}2^{l-1}P \\ &= k_0P + \dots + k_{d-1}2^{d-1}P + k_{(w-1)d}2^{(w-1)d}P + \dots + k_{wd-1}2^{wd-1}P \\ &= k_0P + \dots + (k_{d-1}P)2^{d-1} + k_{(w-1)d}2^{(w-1)d}P + \dots + (k_{wd-1}P)2^{d-1}. \end{aligned}$$

The general idea of the method is to operate on k column by column. First, $k_{d-1}P + \dots + k_{id}2^{id} + \dots + k_{wd-1}2^{wd-1}$ is calculated and doubled. Second, $k_{d-2}P + \dots + k_{id-1}2^{id-1} + \dots + k_{wd-2}2^{wd-2}$ is calculated and added to the results of the first calculation and the final result is doubled. the procedure continues like this.

Finally, $k_0P + \dots + k_{i-1}2^i + \dots + k_{w-1}2^{(w-1)d}$ is calculated and added to the previous sum. The obtained result represents kP . In order to accelerate the computation process, one can compute $[a_{w-1}, \dots, a_2, a_1, a_0]P = a_{w-1}2^{(w-1)d} + \dots + a_12^d + a_0P$ for all possible values of string $(a_{w-1}, \dots, a_2, a_1, a_0)$. The exact algorithm is given below.

Algorithm 3.3.[21]

Fixed-base comb method point multiplication

input: $w, d = \lceil t/w \rceil, k = (k_{t-1}, \dots, k_0)2 = K^{w-1} \parallel \dots \parallel K^0$ where K^i is d -bit integer and k_i^i is t^{th} bit of $k^i, P \in E(F_q)$

Pre computed values: $[a_{w-1}, \dots, a_2, a_1, a_0] P$ for all possible strings (a_{w-1}, \dots, a_0)

output: kP

1. $Q = \infty$
2. For i from $d - 1$ down to 0 do
 - 2.1. $Q = 2Q$
 - 2.2. $Q = Q + [K_i^{w-1}, K_i^{w-2}, \dots, K_i^2, K_i^1]P$
3. Output (Q)

Probability of $[K_i^{w-1}, K_i^{w-2}, \dots, K_i^2, K_i^1]$ is a zero. Hence, it is a non-zero array with a probability of $1 - 1/2^w$. the non-infinite addition in line 2.2

occurs expectedly $(\frac{2^w-1}{2^w}d - 1)$ times (-1 comes from the first infinity addition).

Also, in line 2.1 a non-infinity doubling is executed $(d - 1)$ times (-1 comes from the first infinity doubling). Therefore, the expected running time of Algorithm 3.3 is:

$$\left(\frac{2^w-1}{2^w}d - 1\right)A + (d - 1)D \tag{3.7}$$

In the case of additional memory being available for the algorithm, two columns can be executed simultaneously. The idea is to divide the columns of k into two parts. Apply the previous procedure simultaneously for both sides. This idea is illustrated in Algorithm 3.4

Algorithm 3.4.[21]

Fixed-base comb method point multiplication with two tables

input: $w, d = \lceil l/w \rceil, e = \lceil d/2 \rceil, k = (k_{l-1}, \dots, k_0)2 = K^{w-1} \parallel \dots \parallel K^0$ where K^i is d -bit integer and k_i^i is t^{th} bit of $K^i, P \in E(F_q)$

Pre-computed values: $[a_{w-1}, a_2, a_1, a_0]P$ and $2^e[a_{w-1}, \dots, a_2, a_1, a_0]P$ for all Possible string $(a_{w-1} \dots a_2, a_1, a_0)$

Output: kP

1. $Q = \infty$

2. for i from e-1 down to 0 do

- 2.1. $Q = 2Q$

- 2.2. $Q = Q + [K_i^{w-1}, K_i^{w-2}, \dots, k_i^2, k_i^1]P + 2^e [a_{w-1}, \dots, a_2, a_1, a_0]P$

3. Output (Q)

Similar to Algorithm 3.3, the expected time of Algorithm 3.4 can be calculated as $(\frac{2^w-1}{2^w} 2^e - 1)A + (e - 1)D$. On the other hand, Algorithm 3.4 requires twice as much storage for pre computation as Algorithm 3.3. If memory is limited, the two algorithms can be compared for a given fixed amount of pre computation.

4. Curve Specific Methods

In this section, we will study scalar multiplication methods on some specific curves. These methods developed from special properties of these curves. Specific scalar

multiplication methods are included into many standards, e.g. NIST, IEEE, ISO, ANSI. The methods, studied in the previous two sections, are also applicable to the curves in this section. Moreover, generic methods are used with curve specific methods in order to decrease the computation time of the scalar multiplication.

4.1. Koblitz Curves

The non-supersingular curves defined over F_2 are called Koblitz curves [22], also known as anomalous binary curves. Any non-supersingular curve over F_2 is isomorphic to one of the following two curves:

$$E_0 : y^2 + xy = x^3 + 1$$

$$E_1 : y^2 + xy = x^3 + x^2 + 1.$$

Hence, there exist only two non-isomorphic Koblitz Curves: E_0, E_1 . By simply counting, $\#E_0(F_2) = 4$ and $\#E_1(F_2) = 2$. Then, $\#E_0(F_{2^m}) = 4n$ and $\#E_1(F_{2^m}) = 2n'$, for some n and $n' \in \mathbb{Z}$. In cryptography, n and n' are desired to be prime. n and n' can only be prime if m is prime; otherwise,

there exists a subgroup $E_a(F_{2^d})$ of $E_a(F_{2^m})$ for any $d|m$.

4.2. $Z[\tau]$ and τ – Representation

Let $a \in \{0,1\}$, Then, the Frobenius map on $E_a(F_{2^m})$ is

$$\tau : E_a(F_{2^m}) \rightarrow E_a(F_{2^m})$$

$$\tau(\infty) = \infty$$

$$\tau(x, y) = (x^2, y^2).$$

Squaring in a binary field, when polynomial base is used, requires only insertion of zeros between the components and then reduction. This is very easy compared to other operations. Furthermore, when normal base is used instead of polynomial base, it is just a circular shift. Therefore, computing Frobenius map for any binary field element is very fast. From the previous chapter, it is known that Frobenius map is an endomorphism over $E_a(F_{2^m})$. Its characteristic polynomial is $x^2 - tx + 2$ where t is the trace of Frobenius and equals to $2+1-\# E_a(F_{2^m})$. Explicitly, the characteristic polynomial of

Frobenius map over $E_0(F_{2^m})$ is x^2+x+2 , and similarly, over $E_1(F_{2^m})$ is x^2-x+2 . Let $\lambda = (-1)^{1-a}$, then, the characteristic polynomial of Frobenius over E_a is $x^2 - \lambda x + 2$. Hence,

$$(\tau^2 - \lambda\tau + 2)P = 0 \text{ for all } P \in E_a(F_{2^m})$$

It can be observed that $\tau = (\lambda + \sqrt{7})/2$ is one of the roots of the characteristic polynomial of Frobenius map over $E_a(F_{2^m})$. We can naturally lift the action of Frobenius map to the action of the commutative ring

$$Z[x]/(x^2 - \lambda x + 2) \cong Z[\tau] \text{ by identifying } x \text{ with } \tau : E_a(F_{2^m}) \rightarrow E_a(F_{2^m})$$

This shows that the natural action of $Z[\tau]$ over $E_a(F_{2^m})$ induces the action of the ring $Z[\tau]$ on $E_a(F_{2^m})$:

$$(s_1\tau^l + \dots + s_1\tau + s_0)P = s_1\tau^l(P) + \dots + s_1\tau(P) + s_0P$$

In general, for a scalar multiplication to be efficient, it is preferred to constitute the representation of the scalar as short as possible and small-sparse digits are surely desired. In the following sections, the length and digits of the representation of a scalar are investigated. Any element $\alpha \in Z[\tau]$ can be written uniquely in the

form $\alpha_0 + \alpha_1\tau$ for some integers α_0 and α_1 since $\tau^2 = \lambda\tau - 2$. In order to investigate the digits, we need to know the norm of an element in $Z[\tau]$.

Theorem and proof at [11] enables us to represent any positive integer k in terms of τ

Similar to binary representation of

$$k = \sum_{i=0}^{l-1} k_i 2^i \quad \text{where } k_i \in \{0,1\}, \quad \tau\text{-adic}$$

representation of k can be obtained by repeatedly

dividing k by τ and the digits u_i are remainders of

the division steps. Since $N(\tau) = 2$, remainders

are $-1, 0$ or 1 . Then, any positive integer k can be

represented in τ -adic representation uniquely as

$$k = \sum_{i=0}^{l-1} u_i \tau^i \quad \text{where each digit } u_i \in \{0, \pm 1\}.$$

Any generic method studied in the previous sections

can be applied to τ -adic representation of k . in

order to decrease the number of point additions,

namely, to decrease the number of non-zero

digits, NAF method can be applied to this

representation. It becomes so called τ -adic

NAF or TNAF τ -adic NAF is obtained in a

similar way of 2-adic NAF.

Definition 3.27. TNAF of an element

$$k \in Z[\tau] \text{ is } k = \sum_{i=0}^{l-1} u_i \tau^i \quad \text{where each}$$

$u_i \in \{0, \pm 1\}, u_{i-1} \neq 0$ and no two consecutive digits

are nonzero. The length of the TNAF is l .

Computation of TNAF(τ) is similar to

computation of NAF of an integer.

4.3. Scalar Multiplication on Koblitz Curves

Since a specific coding of k (TNAF(k)) is

obtained, generic methods are applicable in this

case. Algorithm 4.1 performs a scalar

multiplication by using previously observed

properties of the curve and TNAF..

Algorithm 4.1.[11]

TNAF scalar multiplication on Koblitz curves

Input: integer $k \in [1, n - 1]$, $P \in E(F_{2^m})$ of order

n

Output: kP

1. compute $\delta' = k \text{ partmod } p$ by Algorithm 4.3

2. compute $TNAF(\delta') = \sum_{i=0}^{l-1} u_i \tau^i$ by Algorithm 4.2

3. $Q = \infty$

4. for i from $l - 1$ down to 0

4.1 $Q = \tau Q$

4.2 if $u_i = 1$ $Q = Q + P$

4.3 if $u_i = -1$ $Q = Q - P$

5. Output (Q)

Running-time of Algorithm 4.1 is $\frac{1}{3}A$, since τQ is a very fast calculation and the weight of $TNAF(k)$ is $\frac{1}{3}$.

4.4. Window TNAF Method

Window method can be applied to $TNAF(k)$ in order to increase the speed of the algorithm and to secure it against simple power attacks. Similar to width- w NAF method, width- w TNAF method processes w digits of δ^i at a time. However, obtaining width- w TNAF is different. [10]. Algorithm 4.2.[11]

Window TNAF scalar multiplication method for Koblitz curves

input: window width w , integer k in $[1, n - 1]$, $P \in E(\mathbb{F}^{2^m})$ of order n

Output: kP

1. use Algorithm 4.2 to compute $\delta^i = k \text{ part mod } \rho$

2. Use Algorithm 4.3 to compute

$$TNAF_w(\delta^i) = \sum_{i=0}^{l-1} u_i \tau^i$$

3. Compute $Pu = \alpha u P$, $u \in \{1, 3, 5, \dots, 2^{w-1}-1\}$

4. $Q = \infty$

5. For $i = l - 1$ down to 0

5.1 $Q = \tau Q$

5.2 if $u_i \neq 0$

5.2.1 Let u be such that $\alpha u = u_i$ or $\alpha - u = -u_i$

5.2.2 if $u > 0$ then $Q = Q + Pu$

5.2.3 else $Q = Q - P - u$

6. Output (Q)

Pre-computation stage of Algorithm 3.38 costs $(2^{w-2} - 1)A$, while loop costs approximately $\frac{m}{w+1}A$. Thus, the expected running-time of Algorithm 3.4 is

$$(2^{w-2} - 1 + \frac{m}{w+1})A. \tag{4.1}$$

5. Endomorphism

Endomorphism has a crucial role in the theory of elliptic curves. An endomorphism α of an elliptic curve E over a field K is defined as homomorphism on $E(\bar{K})$ given by rational functions. That is,

$$\alpha: E(\bar{K}) \rightarrow E(\bar{K})$$

$$P \rightarrow (g_1(P), g_2(P))$$

Where g_1 and g_2 are rational functions on E (quotient of polynomials) and

$$\alpha(P_1 + P_2) = \alpha(P_1) + \alpha(P_2)$$

for all P_1 and $P_2 \in E(\bar{K})$. The set of all endomorphism of E over K forms a ring under addition and composition [25], called the endomorphism ring of E over K . The characteristic polynomial of an endomorphism α is defined to be the least degree polynomial

$$f(x) \in \mathbb{Z}[x] \text{ satisfying } f(\alpha)(P) = \infty \text{ for all } P \in E,$$

if it exists.

5.1. Endomorphism-Based Scalar Multiplication

Scalar multiplication can be performed faster by using a special endomorphism of the curves. In general, let ϕ be an endomorphism of an elliptic curve $E(\mathbb{F}_q)$ and let $\#E(\mathbb{F}_q)$ be divisible by a prime r , but not by r^2 . Then, by the properties of commutative groups, there exists only one subgroup of order r of $E(\mathbb{F}_q)$. Having prime order implies being a cyclic subgroup. Let it be

generated by P , then, $\phi(P)$ has order r , since ϕ is an endomorphism. Hence, $\phi(P) \in \langle P \rangle$, that is, $\phi(P) = \lambda P$ for some $\lambda \in [1, r - 1]$. In fact, λ is a root modulo r for characteristic polynomial of ϕ .

We can apply above observations to scalar multiplication kP as follows: we find the λ expansion of k . However, since λ is too large, it is desirable to represent k as $k = k_1 + k_2 \lambda \pmod{r}$ where k_1 and k_2 have approximately half the length of k . Then, $kP = k_1P + \lambda k_2P = k_1P + \phi(k_2P)$. Computing $\phi(k_2P)$ is easy; it is just a one field multiplication. So, it is reduced to computing k_1P and k_2P . After finding k_1 and k_2 , applying an interleaved right-to-left scalar multiplication method to k_1P and k_2P reduces the number of doubling operations approximately to half. If k_1 and k_2 for a given k and λ (decomposition of k) can be computed efficiently, The gain is considerable.

5.2. Decomposition of Scalar

It is necessary to find k_1 and k_2 satisfying $k = f(k_1, k_2) = k_1 + k_2 \lambda \pmod{r}$ and number of bits

of k_1 and k_2 are approximately half the number of bits of k , which means that k_1 and k_2 are small or $\sqrt{k_1^2 + k_2^2}$ is small. Thus, the aim is to find a short vector u such that $f(u) = k$. The Trivial solution is $v = (k, 0)$, but this is not short. The approach is as following:

(1) find two vectors $v_1 = (a_1, b_1)$ and $v_2 = (a_2, b_2)$ in $Z \times Z$ satisfying

(i) v_1 and v_2 are linearly independent over R .

(ii) $f(v_1) = f(v_2) = 0$.

(iii) v_1 and v_2 are short themselves, that is,

$\sqrt{a_i^2 + b_i^2}$ is small since a_i and b_i have half the bits of k which can be at least r , then it is approximately \sqrt{r}

(2) find a vector v in the integer lattice generated by v_1 and v_2 that is close to $(k, 0)$. Then, $u = (k, 0) - v$ is a short vector with

$f(u) = f((k, 0)) - f(v) = k - 0 = k$.

Note that sub-problems (1) and (2) may be solved using lattice basis reduction algorithms.

However, [23]'s method is faster.

In Table 4.1, there is a comparison of scalar multiplications in terms of their number of additions, doublings and memory consumption.

5.3. Mutual Opposite Form (Mof)

The left-to-right recoding method eliminates the need for recoding and storing the multiplier in advance. Joye and Yen [27] first proposed the left-to-right recoding algorithm in the year 2000. In CRYPTO2004, Okeya [28] proposed a new efficient left-to-right recoding scheme called *mutual opposite form* (MOF). The unique property of the new signed binary representation is that Signs of adjacent non-zero bits (without considering 0 bits) are opposite. The maximum non-zero bit and the least non-zero bit are 1 and -1, respectively. All the positive integers can be represented by a unique MOF.

5.4. Joint Sparse Form (Jsf)

Solinas presented a right-to-left method called Joint Sparse Form (JSF) for computing the signed binary representation of a pair of integers, which results in a minimal joint weight

compared to Shamir's method. The property of JSF is that the average joint weight among all JSF representations of two n -bit integers is $n/2$. Of any three consecutive positions, at least one is a double zero. Adjacent terms do not have opposite

- signs, that is $X_j, X_{j+1} \neq -1$ and $Y_j, Y_{j+1} \neq -1$
- If $X_j, X_{j+1} \neq -1$, then $Y_{j+1} = \pm 1$ and $Y_j = 0$
- If $Y_j, Y_{j+1} \neq -1$, then $X_{j+1} = \pm 1$ and $X_j = 0$

The Algorithm is used for generating joint sparse form of a pair of integers called JSF of integers.

5.5. Direct Doubling

Among the various elliptic curve arithmetic operations, point doubling is quite costlier than point addition in the scalar multiplication over coordinate system. Sakai and Sakurai proposed a scalar multiplication algorithm using direct computation of several doublings (which

computes $2kP$ directly from P) without computing the intermediate points $2P, 2^2P, 2^3P, \dots, 2^{k-1}P$. The concept of direct computation of $2kP$ was first suggested by Guajardo and Paar. The new doubling formula was reconstructed as below.

$$A1 = x1$$

$$B1 = 3x1^2 + a$$

$$C1 = -y1$$

$$D1 = 12A1 C1^2 - B1^2$$

$$x2 = B1^2 \cdot 8A1 C1^2 / (2C1)^2$$

$$y2 = 8C1^4 \cdot B1 D1 / (2C1)^3$$

The computational complexity of this formula is $(5S + 5M + I)$ and the existing method has the complexity of $(6S + 4M + I)$.

Table 1 – Comparison of Different Algorithms

Algorithm	Addition	Doublings	Memory
3.2	$l/2$	l	-
2.5	$l/3$	l	-
2.7	$2^{w-2} + \frac{l}{w+1} - 1$	$l + 1$	2^{w-2}

2.8	$\frac{2^w - (-1)^w}{3} + \frac{1}{w + v(w)} - 1$	$l + 1$	2^{w-2}
3.1	$(2^w + d - 3), d = \left\lfloor \frac{l}{w} \right\rfloor$	-	$\left\lfloor \frac{l}{w} \right\rfloor$
3.2	$\left(\frac{2^{w+1}}{3} + d - 2\right), d = \left\lfloor \frac{l+1}{w} \right\rfloor$	-	$\left\lfloor \frac{l+1}{w} \right\rfloor$
3.3	$\left(\frac{2^w - 1}{2^w} d - 1\right), d = \left\lfloor \frac{l}{w} \right\rfloor$	$(d-1)$	2^w
3.4	$\left(\frac{2^w - 1}{2^w} 2e - 1\right), e = \left\lfloor \frac{l}{2w} \right\rfloor$	$(e-1)$	2^{w+1}
4.1	$l/3$	-	-

6. Conclusion

In this paper, we studied the scalar multiplication on elliptic curves. We made mathematical and computational analysis of many scalar multiplication methods.

In order to develop a scalar multiplication method or to improve an existing one, we first need to analyze the properties of elliptic curves carefully and then consider whether a recoding method of an integer corresponds to an efficient scalar multiplication method, and finally, use some algebraic methods on elliptic curves.

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